

Code: 9A05406

R09

B.Tech II Year II Semester (R09) Supplementary Examinations May/June 2015

COMPUTER ORGANIZATION

(Common to CSE & ECC)

Time: 3 hours

Max. Marks: 70

Answer any FIVE questions
All questions carry equal marks

- 1 Explain with examples how errors can be detected using the parity bit concept.
- 2 (a) What are the major characteristics of CISC and RISC architectures?
(b) List data manipulation instructions and explain with example.
- 3 Write short notes on the following:
(a) Hardwired control.
(b) Microprogrammed control
- 4 List the essential steps required in performing division operation on floating point numbers. Explain them in detail.
- 5 Explain the various features and applications of DVD
- 6 Discuss about various data transfer modes used in DMA.
- 7 (a) Write short notes on array processors
(b) Give examples for super computers and explain them in detail
- 8 Discuss in detail about inter processor communication and synchronization
